Refine Search

Search Results -

| . Term | Documents |
|---|-----------|
| CYCLE | 1179155 |
| CYCLES | 484110 |
| OUTPUT | 3566765 |
| OUTPUTS | 1065909 |
| REPET\$5 | 0 |
| REPET | 736 |
| REPETA | 18 |
| REPETAB | 1 |
| REPETABLE | 3 |
| REPETABLY | 2 |
| REPETABS | 8 |
| ((L6 OR L4) AND (REPET\$5 ADJ3 CYCLE ADJ5 OUTPUT)).PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD. | 0 |

There are more results than shown above. Click here to view the entire set.

| Database: | US Pre-Grant Publication Full-Text Database US Patents Full-Text Database US OCR Full-Text Database EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index IBM Technical Disclosure Bulletins | |
|---------------|--|---------------|
| Search: | L10 | Refine Search |
| | Recall Text Clear | Interrupt |
| | Search History | |
| ATE: Tuesday, | September 06, 2005 Printable Copy Create Case | |

<u>Set</u>

side by

side

Name Query

<u>Name</u>

result set

<u>Hit</u>

Count

| DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR | | | | | | | |
|--|------------|---|-----|------------|--|--|--|
| | <u>L10</u> | (L6 or L4) and (repet\$5 adj3 cycle adj5 output) | 0 | <u>L10</u> | | | |
| | <u>L9</u> | (L6 or L4) and (offset\$4 adj3 counter) | 0 | <u>L9</u> | | | |
| | <u>L8</u> | (L6 or L4) and (internal adj3 interrupt\$3 adj5 processor) | . 0 | <u>L8</u> | | | |
| | <u>L7</u> | (L6 or L4) and (interrupt\$3 adj5 position) | 4 | <u>L7</u> | | | |
| DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=NO; OP=OR | | | | | | | |
| | <u>L6</u> | (programmable adj controller) and (position adj5 counter) and (transducer) and (forward adj3 position) | 22 | <u>L6</u> | | | |
| | <u>L5</u> | (programmable adj controller) and (position adj5 counter) and (transducer) and (net adj3 forward adj3 position) | 0 | <u>L5</u> | | | |
| | <u>L4</u> | (programmable adj controller) and (position adj5 counter) and (transducer) and output | 42 | <u>L4</u> | | | |
| | <u>L3</u> | (programmable adj controller) and (output adj3 state) and (position adj5 counter adj5 transducer) | 0 | <u>L3</u> | | | |
| | <u>L2</u> | (programmable adj controller) and (output adj3 state) and (position adj3 transducer adj5 counter) and interface | 0 | <u>L2</u> | | | |
| | <u>L1</u> | (programmable adj controller) and (output adj3 state) and (position adj3 transducer) and interface and counter | 4 | <u>L1</u> | | | |

END OF SEARCH HISTORY